

FIG. 1a

APPROVED	O.G. FIG.
BY	CLASS/SUBC
DRAFTSMAN	

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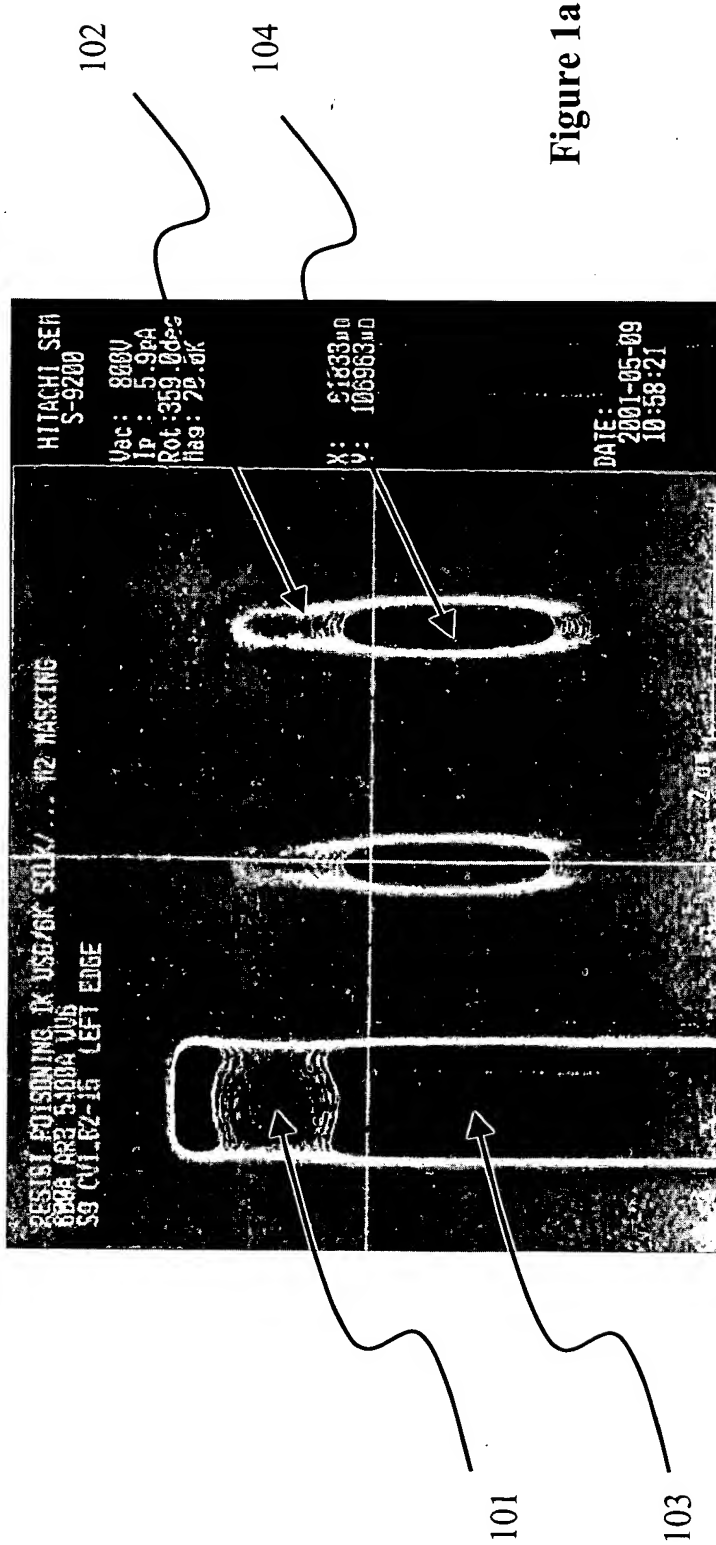


Figure 1a

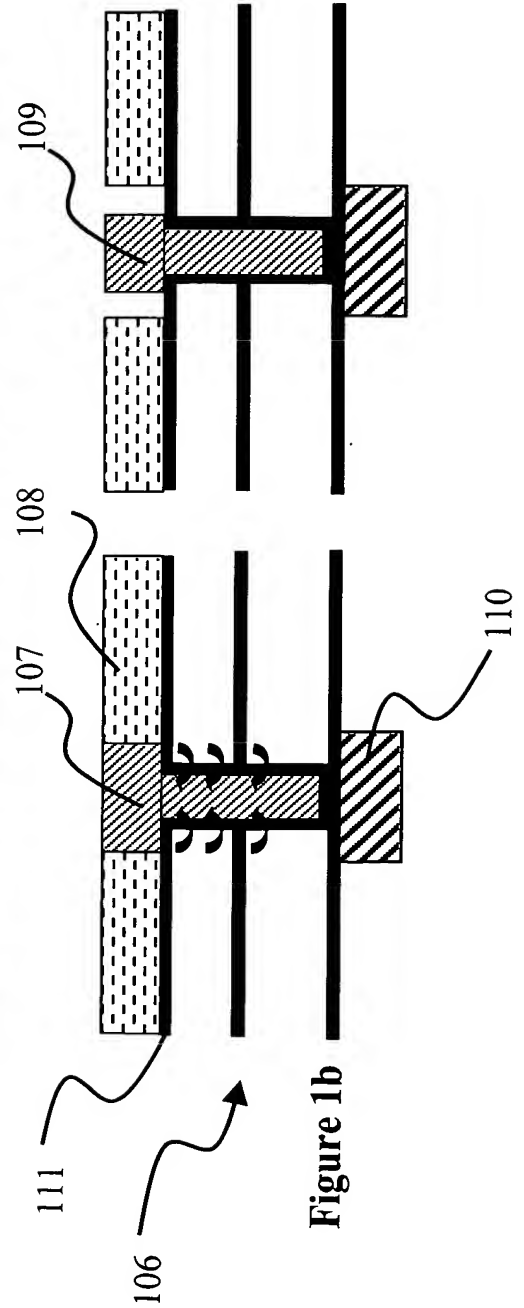


Figure 1b

Figure 1c

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APPROVED	O.G. FIG.
BY	CLASS/SUBC.
CRAFTSMAN	

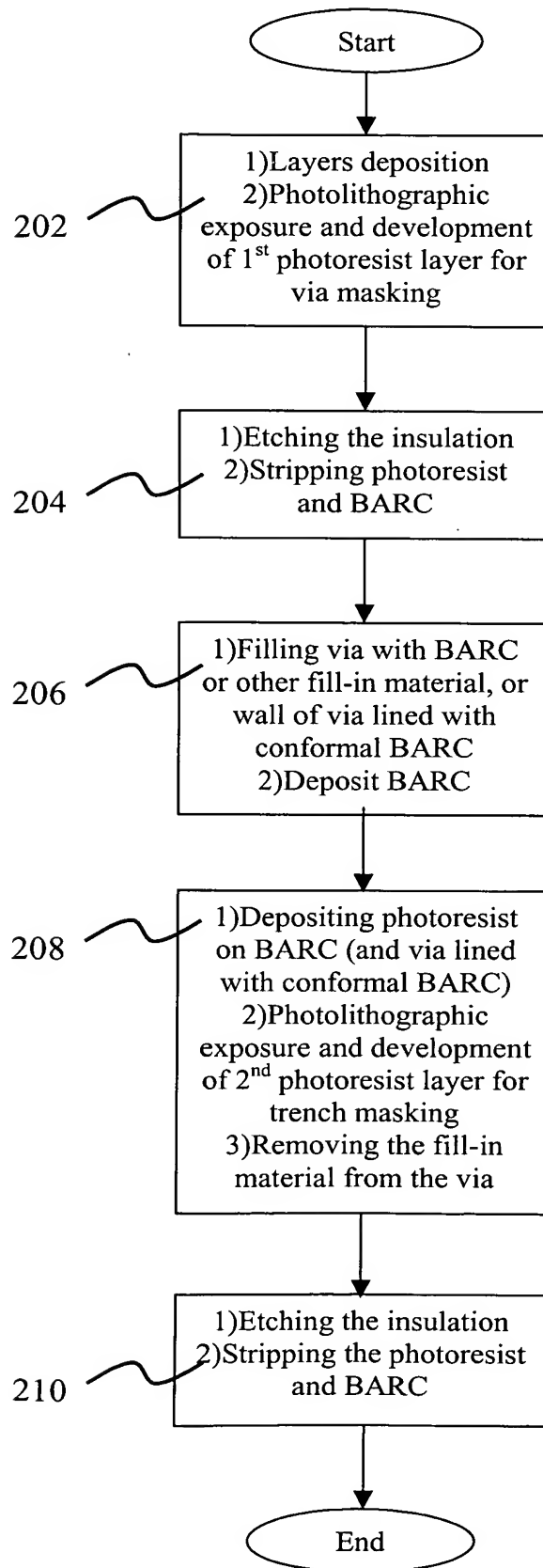


Figure 2

APPROVED	O.G. F.E.
BY	CLASS/SUBC
CRAFTSMAN	

RESIST POISONING IN USB/GR SILICON WZ MASKING

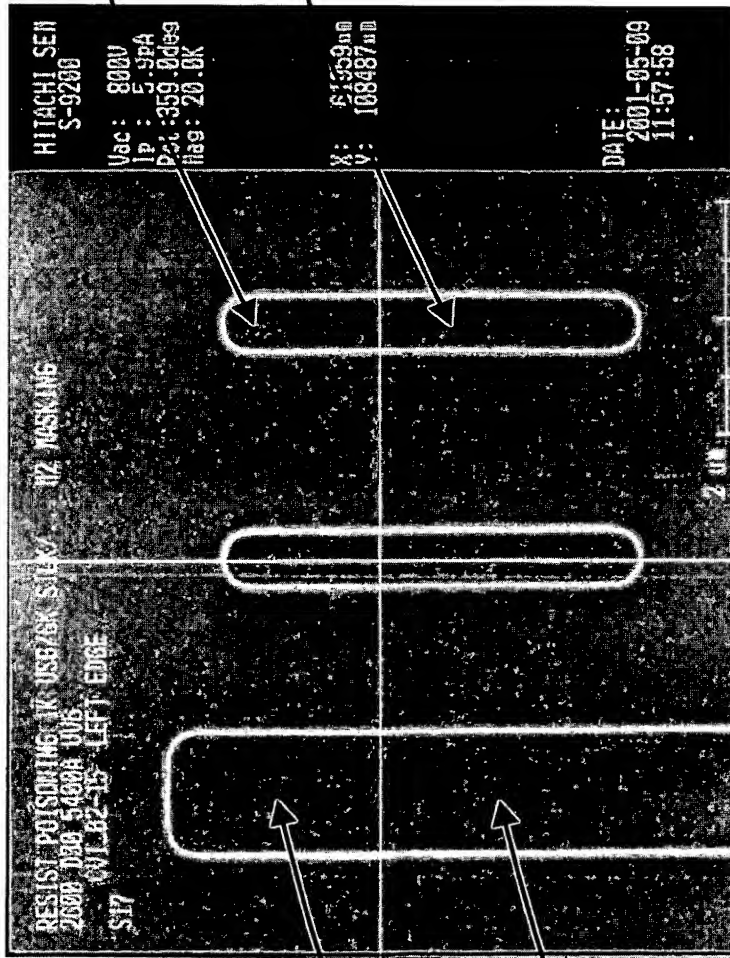


Figure 3a

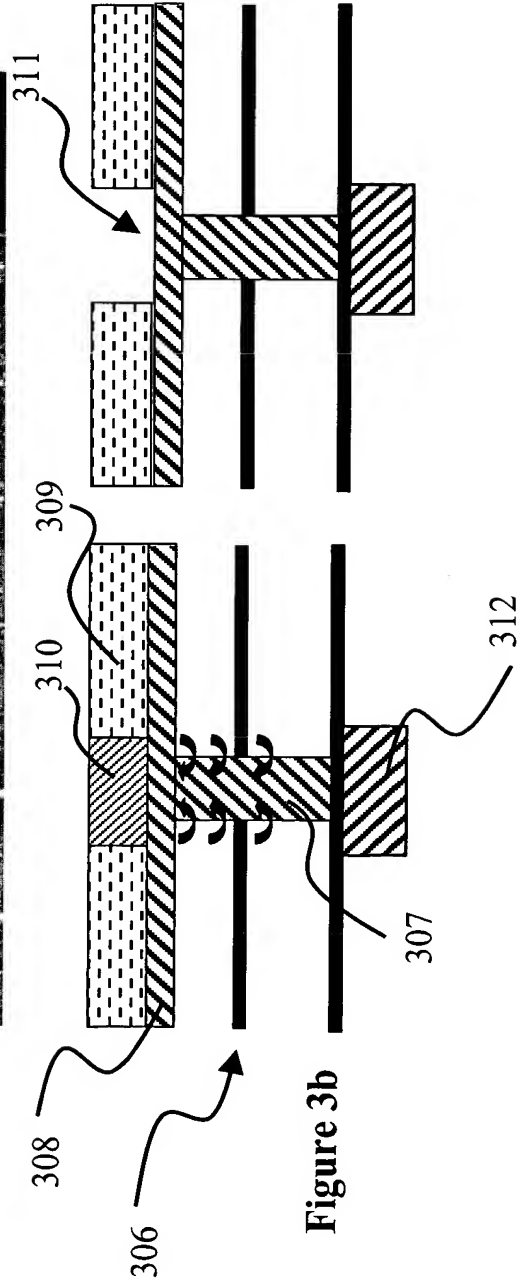


Figure 3b

Figure 3c

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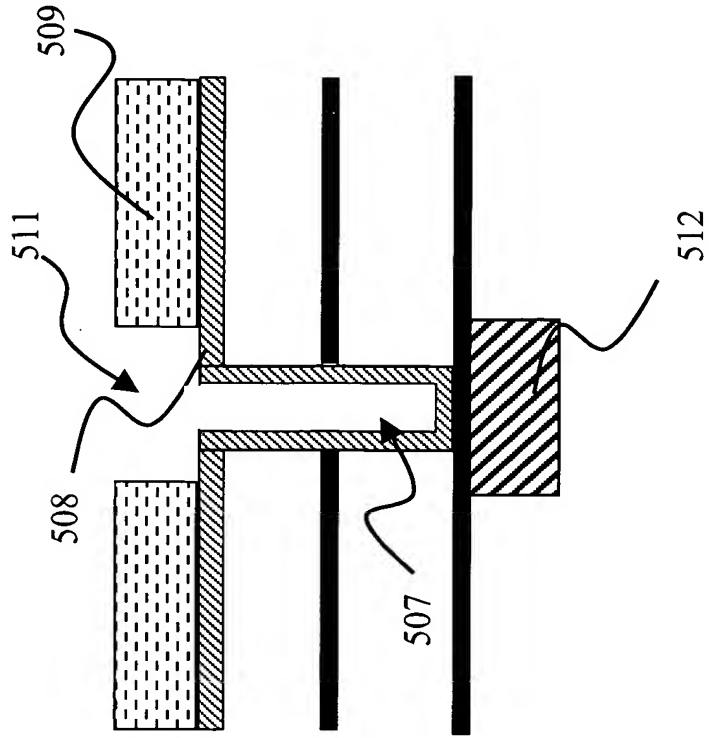


Figure 5

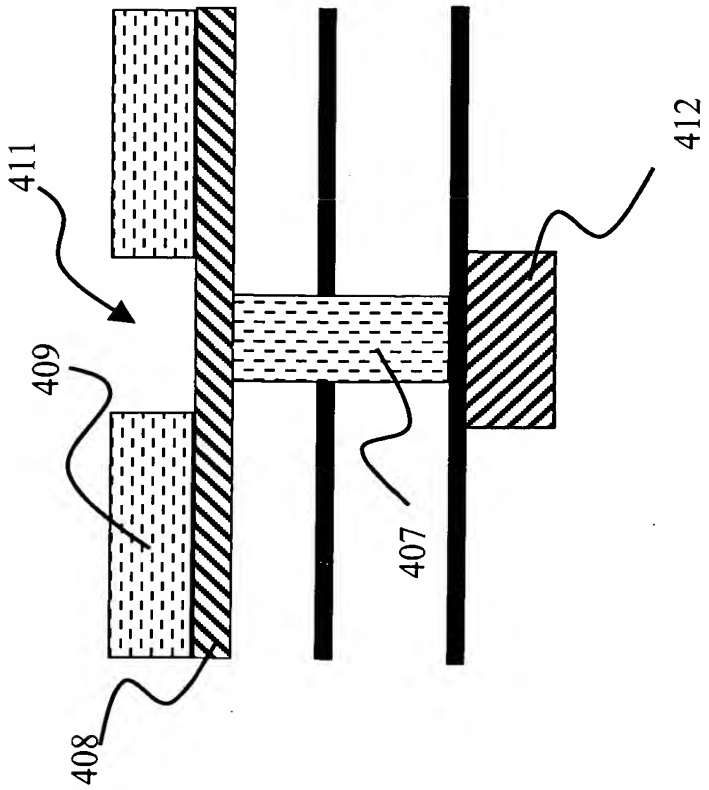


Figure 4

Patent # 5805200